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A L_1 adaptive controller implementation in Simulink

Figure A.1 shows the complete Simulink block diagram of the implementation of the L_1 controller used in this thesis as well as the PID controller used for comparison. Figure A.2 shows the internal parts of the block: L1 Adaptive Controller2. Figure A.3 the three subsystems of fig. A.2.

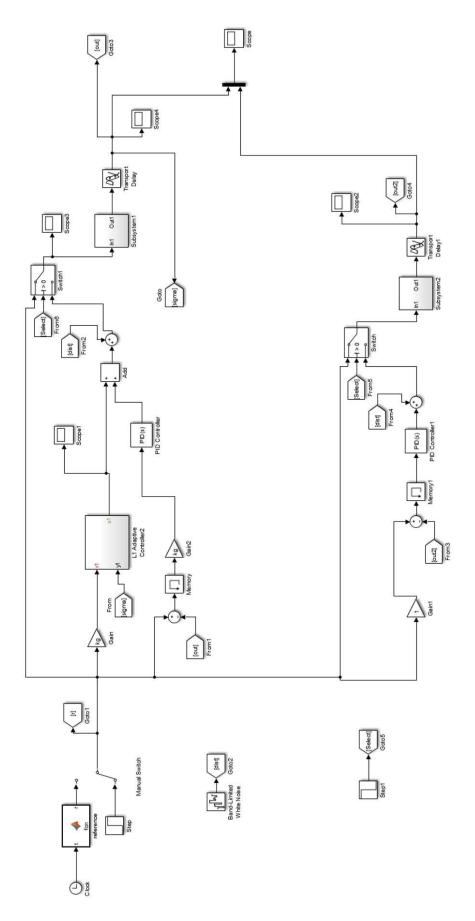


Figure A.1: Block diagram of L_1 controller and PID

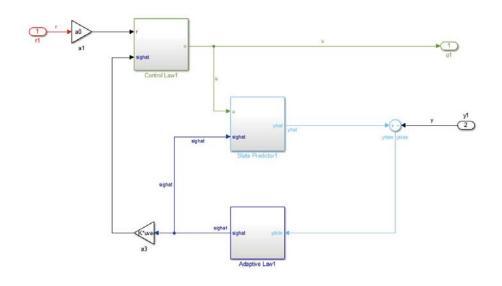


Figure A.2: Block diagram of L_1 controller

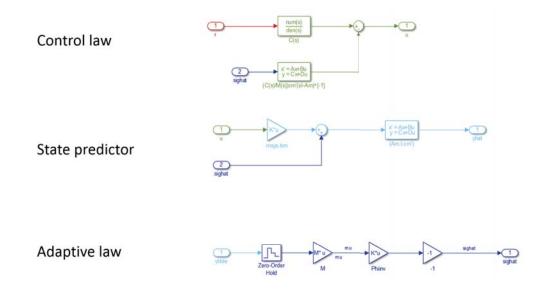


Figure A.3: Subsystems block diagram of L_1 controller