## VII

# The implementation of Céu

The compilation process of a program in CÉU is composed of three main phases, as illustrated in Figure VII.1:

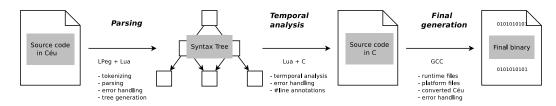


Figure VII.1: Compilation process: from the source code in Céu to the final binary.

Parsing The parser of Céu is written in LPeg [24], a pattern matching library that also recognize grammars, making it possible to write the tokenizer and grammar with the same tool. The source code is then converted to an abstract syntax tree (AST) to be used in further phases. This phase may be aborted due to syntax errors in the Céu source file.

Temporal analysis This phase detects inconsistencies in CÉU programs, such as unbounded loops and the forms of non-determinism. It also makes some "classical" semantic analysis, such as building a symbol table for checking variable declarations. However, most of type checking is delayed to the last phase to take advantage of GCC's error handling. Therefore, this phase needs to annotate the C output with #line pragmas that match the original file in CÉU. This phase must output code in C, given how tied CÉU is to C by design.

Final generation The final phase packs the generated C file with the CÉU runtime and platform-dependent functionality, compiling them with gcc and generating the final binary. The CÉU runtime includes the scheduler, timer management, and the external C API. The platform files include libraries for I/O and bindings to invoke the CÉU scheduler on external events.

In the sections that follow, we discuss the most sensible parts of the compiler considering our design, such as the temporal analysis, runtime scheduler, and the external API.

## VII.1 Temporal analysis

As introduced, the *temporal analysis* phase detects inconsistencies in Céu programs. Here, we focus on the algorithm that detects non-deterministic access to variables, as presented in Section III.2.

For each node representing a statement in the program AST, we keep the set of events I (for incoming) that can lead to the execution of the node, and also the set of events O (for outgoing) that can terminate the node.

A node inherits the set I from its direct parent and calculates O according to its type:

- Nodes that represent expressions, assignments, C calls, and declarations simply reproduce O = I, as they do not await;
- An await e statement has  $O = \{e\}$ .
- A break statement has  $O = \{\}$  as it escapes the innermost loop and never terminate, i.e., never proceeds to the statement immediately following it (see also loop below);
- A sequence node (;) modifies each of its children to have  $I_n = O_{n-1}$ . The first child inherits I from the sequence parent, and the set O for the sequence node is copied from its last child, i.e.,  $O = O_n$ .
- A loop node includes its body's O on its own I ( $I = I \cup O_{body}$ ), as the loop is also reached from its own body. The union of all break statements' O forms the set O for a loop.
- An if node has  $O = O_{true} \cup O_{false}$ .
- A parallel composition (par/and / par/or) may terminate from any of its branches, hence  $O = O_1 \cup ... \cup O_n$ .

With all sets calculated, any two nodes that perform side effects and are in parallel branches can have their I sets compared for intersections. If the intersection is not the empty set, they are marked as suspicious (see Section III.2).

Figure VII.2 reproduces the second code of Figure III.5 and shows the corresponding AST with the sets I and O for each node. The event . (dot) represents the "boot" reaction. The assignments to y in parallel (lines 5,8 in the code) have an empty intersection of I (lines 6,9 in the AST), hence, they

do not conflict. Note that although the accesses in lines 5, 11 in the code (lines 6,11 in the AST) do have an intersection, they are not in parallel and are also safe.

```
input void A, B;
                                          Stmts I=\{.\} O=\{A\}
                                      1
var int y;
                                      2
                                              Dcl_y I={.} O={.}
par/or do
                                              ParOr I=\{.\} O=\{A,B\}
                                      3
  await A;
                                                   Stmts I=\{.\} O=\{A\}
                                      4
  y = 1;
                                      5
                                                        Await_A I=\{.\} O=\{A\}
with
                                                        Set_y I={A} O={A}
  await B;
                                                   Stmts I=\{.\} O=\{B\}
                                      7
  y = 2;
                                                        Await_B I=\{.\} O=\{B\}
                                      8
end
                                      9
                                                        Set_y I=\{B\} O=\{B\}
await A;
                                     10
                                              Await_A I=\{A,B\} O=\{A\}
y = 3;
                                              Set_y I={A} O={A}
                                     11
```

Figure VII.2: A program with a corresponding AST describing the sets I and O. The program is safe because accesses to y in parallel have no intersections for I.

## VII.2 Memory layout

CÉU favors a fine-grained use of trails, being common the use of trails that await a single event. For this reason, CÉU does not allocate per-trail stacks; instead, all data resides in fixed memory slots—this is true for the program variables as well as for temporary values and flags needed during runtime. Memory for trails in parallel must coexist, while statements in sequence can reuse it. CÉU reserves a single static block of memory to hold all memory slots, whose size is the maximum the program uses at a given time. A given position in the memory may hold different data (with variable sizes) during runtime.

Translating this idea to C is straightforward [28, 5]: memory for blocks in sequence are packed in a struct, while blocks in parallel, in a union. As an example, Figure VII.3 shows a program with corresponding memory layout. Each variable is assigned a unique id (e.g. a\_1) so that variables with the same name can be distinguished. The do-end blocks in sequence are packed in a union, given that their variables cannot be in scope at the same time, e.g., MEM.a\_1 and MEM.b\_2 can safely share the same memory address. The example also illustrates the presence of runtime flags related to the parallel composition, which also reside in reusable slots in the static memory.

#### VII.3 Trail allocation

The compiler extracts the maximum number of trails a program can have at the same time and creates a static vector to hold runtime information about

```
union {
input int A, B, C;
                                                      // sequence
                                     int a_1;
                                                      //
                                                            do_1
                                                            do_2
    var int a = await A;
                                     int b_2;
                                                            par/and
end
                                     struct {
                                                      //
do
                                         u8 _and_3: 1;
    var int b = await B;
                                         u8 _and_4: 1;
end
                                     };
par/and do
                                } MEM ;
    await B;
with
    await C;
end
```

Figure VII.3: A program with blocks in sequence and in parallel, with corresponding memory layout.

them. Again, trails that cannot be active at the same time can share memory slots in the static vector.

At any given moment, a trail can be awaiting in one of the following states: INACTIVE, STACKED, FIN, or in any event defined in the program:

All terminated or not-yet-started trails stay in the INACTIVE state and are ignored by the scheduler. A STACKED trail holds its associated stack level and is delayed until the scheduler runtime level reaches that value again. A FIN trail represents a hanged finalization block which is only scheduled when its corresponding block goes out of scope. A trail waiting for an event stays in the state of the corresponding event, also holding the sequence number (seqno) in which it started awaiting. A trail is represented by the following struct:

```
struct trail_t {
    state_t evt;
    label_t lbl;
    union {
        unsigned char seqno;
        stack_t stk;
    };
};
```

The field evt holds the state of the trail (or the event it is awaiting); the field 1b1 holds the entry point in the code to execute when the trail is

```
input void A;
                                         enum {
1
    event void e;
                                           Main = 1,
                                                          // ln
2
                                                                  7
      TRAIL 0 - lbl Main
                                                              ln
3
                                           Awake_e,
                                                          // ln
    par/and do
                                           ParAnd chk,
                                                                 8,
                                                                     15
4
                                                          // ln 10
      // TRAIL 0 - lbl Main
                                           ParAnd_sub_2,
5
                                           Awake_A_1,
                                                          // ln 12
      await e;
      // TRAIL 0 - lbl Awake_e
                                           Emit_e_cont,
                                                          // ln 14
7
      // TRAIL 0 - lbl ParAnd_chk
                                           ParAnd_out,
                                                          // ln 17
8
                                                          // ln 19
                                           Awake_A_2
9
    with
         TRAIL 1 - lbl ParAnd_sub_2
10
11
      await A;
      // TRAIL 1 - lbl Awake_A_1
12
      emit e;
13
      // TRAIL 1 - lbl Emit e cont
14
      // TRAIL 1 - lbl ParAnd chk
15
    end
16
    // TRAIL 0 - lbl ParAnd_out
17
    await A;
18
    // TRAIL 0 - lbl Awake_A_2
19
```

Figure VII.4: Static allocation of trails and entry-point labels.

scheduled; the third field depends on the evt field and may hold the seqno for an event, or the stack level stk for a STACKED state.

The size of state\_t depends on the number of events in the application; for an application with less than 253 events (plus the 3 states), one byte is enough. The size of label\_t depends primarily on the number of await statements in the application—each await splits the code in two and requires a unique entry point in the code for its continuation. Additionally, split & join points for parallel compositions, emit continuations, and finalization blocks also require labels. The seqno will eventually overflow during execution (every 256 reactions). However, given that the scheduler traverses all trails in each reaction, it can adjust them to properly handle overflows (actually 2 bits to hold the seqno would be already enough). The stack size depends on the maximum depth of nested emissions and is bounded to the maximum number of trails, e.g., a trail emits an event that awakes another trail, which emits an event that awakes another trail, and so on—the last trail cannot awake any trail, because they will be all hanged in a STACKED state. In WSNs applications, the size of trail\_t is typically only 3 bytes (1 byte for each field).

## (a) Code generation

The example in Figure VII.4 illustrates how trails and labels are statically allocated in a program. The program has a maximum of 2 trails, because the par/and (line 4) can reuse  $TRAIL \ \theta$ , and the join point (line 16) can reuse both  $TRAIL \ \theta$  and  $TRAIL \ 1$ . Each label is associated with a unique identifier

```
while (<...>) {
                                   // scheduler main loop
1
       trail_t* trail = <...>
                                   // choose next trail
2
       switch (trail->lbl) {
3
          case Main:
4
              // activate TRAIL 1 to run next
5
             TRLS[1].evt = STACKED;
              TRLS[1].lbl = ParAnd_sub_2; // 2nd trail of par/and
              TRLS[1].stk = current_stack;
8
9
              // code in the 1st trail of par/and
10
              // await e;
11
              TRLS[0].evt = EVT e;
12
              TRLS[0].lbl = Awake_e;
13
              TRLS[0].seq = current_seqno;
14
             break;
15
16
          case ParAnd_sub_2:
17
              // await A;
18
              TRLS[1].evt = EVT_A;
19
              TRLS[1].lbl = Awake_A_1;
20
              TRLS[1].seq = current_seqno;
^{21}
                 break;
22
23
                     // other labels
            <...>
24
25
        }
26
```

Figure VII.5: Generated code for the program of Figure VII.4.

in the enum. The static vector to hold the two trails in the example is defined as

```
trail_t TRLS[2];
```

In the final generated C code, each label becomes a *switch case* working as the entry point to execute its associated code. Figure VII.5 shows the corresponding code for the program of Figure VII.4. The program is initialized with all trails set to INACTIVE. Then, the scheduler executes the Main label in the first trail. When the Main label reaches the par/and, it "stacks" the 2nd trail of the par/and to run on  $TRAIL\ 1$  (line 5-8) and proceeds to the code in the 1st trail (lines 10-15), respecting the deterministic execution order. The code sets the running  $TRAIL\ 0$  to await EVT\_e on label Awake\_e, and then halts with a break. The next iteration of the scheduler takes  $TRAIL\ 1$  and executes its registered label ParAnd\_sub\_2 (lines 17-22), which sets  $TRAIL\ 1$  to await EVT\_A and also halts.

Regarding cancellation, trails in parallel are always allocated in subsequent slots in the static vector TRLS. Therefore, when a par/or terminates, the scheduler sequentially searches and executes FIN trails within the range of the par/or, and then clears all of them to INACTIVE at once. Given that finalization

blocks cannot contain await statements, the whole process is guaranteed to terminate in bounded time. Escaping a loop that contains parallel compositions also trigger the same process.

#### VII.4 The external C API

As a reactive language, the execution of a program in CÉU is guided entirely by the occurrence of external events. From the implementation perspective, there are three external sources of input into programs, which are all exposed as functions in a C API:

- ceu\_go\_init(): initializes the program (e.g. trails) and executes the "boot"
  reaction (i.e., the Main label).
- ceu\_go\_event(id,param): executes the reaction for the received event id
   and associated parameter.
- ceu\_go\_wclock(us): increments the current time in microseconds and runs a reaction if any timer expires.

Given the semantics of Céu, the functions are guaranteed to take a bounded time to execute. They also return a status code that says if the Céu program has terminated after the reactions. Further calls to the API have no effect on terminated programs.

The bindings for the specific platforms are responsible for calling the functions in the API in the order that better suit their requirements. As an example, it is possible to set different priorities for events that occur concurrently (i.e. while a reaction chain is running). However, a binding must never interleave or run multiple functions in parallel. This would break the Céu sequential/discrete semantics of time.

As an example, Figure VII.6 shows our binding for TinyOS which maps nesC callbacks to input events in Céu. The file ceu.h (included in line 3) contains all definitions for the compiled Céu program, which are further queried through #ifdef's. The file ceu.c (included in line 4) contains the main loop of Céu pointing to the labels defined in the program. The callback Boot.booted (lines 6-11) is called by TinyOS on mote startup, so we initialize Céu inside it (line 7). If the Céu program uses timers, we also start a periodic timer (lines 8-10) that triggers callback Timer.fired (lines 13-17) every 10 milliseconds and advances the wall-clock time of Céu (line 15)<sup>1</sup>. The remaining

<sup>&</sup>lt;sup>1</sup>We also offer a mechanism to start the underlying timer on demand to avoid the "battery unfriendly" 10ms polling.

lines map pre-defined TinyOS events that can be used in Céu programs, such as the light sensor (lines 19-23) and the radio transceiver (lines 25-36).

```
implementation
1
^{2}
         #include "ceu.h"
         #include "ceu.c"
4
5
        event void Boot.booted () {
6
             ceu_go_init();
    #ifdef CEU_WCLOCKS
8
             call Timer.startPeriodic(10);
9
    #endif
10
11
        }
12
    #ifdef CEU_WCLOCKS
13
        event void Timer.fired () {
14
             ceu_go_wclock(10000);
15
16
    #endif
17
18
    #ifdef _EVT_PHOTO_READDONE
19
        event void Photo.readDone (uint16_t val) {
20
             ceu_go_event(EVT_PHOTO_READDONE, (void*) val);
^{21}
22
23
    #endif
24
    #ifdef _EVT_RADIO_SENDDONE
^{25}
26
        event void RadioSend.sendDone (message_t* msg) {
             ceu_go_event(EVT_RADIO_SENDDONE, msg);
27
28
    #endif
^{29}
30
    #ifdef _EVT_RADIO_RECEIVE
31
        event message_t* RadioReceive.receive (message_t* msg) {
32
             ceu_go_event(EVT_RADIO_RECEIVE, msg);
33
             return msg;
34
35
    #endif
36
37
                 // other events
        <...>
38
39
```

Figure VII.6: The TinyOS binding for CÉU.