8 Referências Bibliográficas

[1] TSUI, J. B. **Microwave Receiver with Electronic Warfare Applications**. Malabar, Florida, USA: Krieger Publishing Company, 1992. pp. 1-8.

[2] TSUI, J. B. **Digital Techniques for Wideband Receivers.** Norwood, MA, USA: Artech House, 1995. pp. 19-26.

[3] SULLIVAN, W. B. High Clock Rates Advance IFM/DFD Technology. **Microwaves & RF**, vol. 33, no. 6, June, 1994, pp. 146-148.

[4] SULLIVAN, W. B. Instantaneous Frequency Measurement Receivers for Maritime Patrol. **The Journal of Electronic Defense**, vol. 25, no. 10, October, 2002, pp. 55-56,58.

[5] HARRISON, R. G.; TUCKER, T.W. Frequency Division Solve Systems Problems. **Microwave Systems News**, pp. 97-101, October, 1978.

[6] TUCKER, T. W. **Countermeasures System**. USA Patent no. 4,396,917, 2nd August 1983.

[7] CORNISH, W. D. Microwave Frequency Dividers, Devices and Applications. In: 1980 Military Microwave Conference, 1980, London. **Proceedings…** London, England: EUREL-The Convention of National Societies of Electrical Engineers of Western Europe, 1980, pp. 13-18.

[8] RAUSCHER, C. Regenerative Frequency Division with GaAs FET. **IEEE Transactions or Microwave Theory and Techniques**, vol. 32, no. 11, November, 1984, pp. 1461-1468.

[9] RYLYAKOV, A.; ZWICK, T. 96-GHz Static Frequency Divider in SiGe Bipolar Technology. **IEEE Journal of Solid State Circuits**, vol. 39, no. 10, October, 2004, pp. 1712-1715.

[10] HESHMATI, Z.; HUNTER, I.; POLLARD, R. MMIC Frequency Dividers. In: 1st EMRS DTC Technical Conference, 2004, Edinburgh. **Proceedings…** Edinburgh, England: EMRS DTC, 2004.

[11] BAHL, I.; BHARTIA, P. **Microwave Solid-State Circuit Design**. New York, USA: John Wiley & Sons, 1988. pp. 717-744.

[12] MC CONNEL, J. W. A Parametric Frequency Divider Using Hyperabrupt Junction Diode Capacitance. **IEEE Journal of Solid-State Circuits**, 1968, pp. 311-312.

[13] HARRISON, R. G. A Broad-Band Frequency Divider Using Microwave Varactors. **IEEE *Transactions* on Microwave Theory and Techniques**, vol. MTT-25, no. 12, December, 1977, pp. 1055-1059.

[14] KARADENIZ, M. Novel Wideband Frequency Divider Employing Two Step-Recovery Diodes. **Electronics Letters**, vol. 10, no. 14, July, 1974, pp. 283-285.

[15] FORTESCUE, R. L. Quasi-Stable Frequency-Dividing Circuits. **Journal of IEE**, vol. 84, 1939, pp. 693-698.

[16] MILLER, R. L. Fractional-Frequency Generators Utilizing Regenerative Modulation. **Proceedings of the IRE**, vol. 27, no. 7, July, 1939, pp. 446-457.

[17] UPADHYAYULA, L. C.; NARAYAN, S. Y. Microwave Frequency Division Using Transferred Electron Devices. **Electronics Letters**, vol. 9, no. 2, February, 1973, pp. 85-86.

[18] HUANG, C. J. *et al.* Dynamic Microwave Frequency Division Characteristics of Coplanar Transferred-Electron Devices. **IEEE Transactions on Microwave theory and Techniques**, vol. MTT-24, no. 1, January, 1976, pp. 61-63.

[19] AHAMED, S. V.; IRVIN, J. C.; SEIDEL, H. Study and Fabrication of a Frequency Divider-Multiplier Scheme for High-Efficiency Microwave Power. **IEEE Transactions on Communications**, vol. COM-24, February, 1976, pp. 243-249.

[20] WU, H.; ZHANG, L. A 16-to-18-GHz 0.18 μm Epi-CMOS Divide-by-3 Injection-Locked Frequency Divider. In: 2006 IEEE International Solid-State Circuits Conference, ISSCC 2006, 2006, San Francisco. **Digest…** California, USA: IEEE Solid-State Circuits Society, 2006, pp. 27-29.

[21] RATEGH, H. R.; LEE, T. H. Superharmonic Injection Locked Oscillators as Low Power Frequency Dividers. In: 1998 Symposium on VLSI Circuits, 1998, Honolulu. **Digest…** Hawaii, USA: IEEE Solid-State Circuits Society and Japan Society of Applied Physics, 1998, pp. 132-135.

[22] JEONG, J.; KWON, Y. V-Band High-Order Harmonic Injection-Locked Frequency-Divider MMICs with Wide Bandwidth and Low-Power Dissipation. **IEEE Transactions on Microwave Theory and Techniques**, vol. 53, no. 6, June, 2005, pp. 1891-1897.

[23] YAMAMOTO, K.; FUJISHIMA, M. 55 GHz CMOS Frequency Divider with 3.2 GHz Locking Range. In: 30th European Solid-State Circuits Conference, ESSCIRC 2004, 2004, Leuven. **Proceedings…** Belgium: IEEE Solid-State Circuits Society, 2004, pp. 135-138.

[24] ESKELINEN, H.; ESKELINEN P. An Experimental Arrangement for Injection Locking Ka-Band Oscillators. In: 2003 IEEE International Frequency Control Symposium and PDA Exhibition Jointly with the 17th European Frequency and Time Forum, 2003, Tampa. **Proceedings…** Florida, USA: IEEE Ultrasonics, Ferroeletrics, and Frequency Control Society, 2003, pp. 524-527.

[25] ZHANG, X.; GRESHAM, I. An Analogue Frequency-Division Approach for Subharmonic Generation in Microwave VCOs. In: 1998 IEEE MTT-S International Microwave Symposium, 1998, Baltimore. **Digest…**

Maryland, USA: IEEE Microwave Theory and Techniques Society, 1998, pp. 1581-1584.

[26] KAWANO, Y. *et al*. High-Speed Operation of a novel Frequency Divider Using Resonant Tunneling Chaos Circuit. In: 2001 International Conference on Indium Phosphide and Related Materials, 2001, Nara. **Proceedings…** Japan: IEEE Photonics Society, 2001, pp. 236-239.

[27] HILIBRAND, J.; BEAM, W. R. Semiconductor Diodes in Parametric Subharmonic Oscillators. **RCA Review**, June, 1959, pp. 229-253.

[28] SOARES, R. (Ed.). **Ga As MESFET Circuit Design**. Norwood, MA, USA: Artech House, 1988. pp. 442-448.

[29] FEI, L. F. Frequency Divider Design Strategies. **RFdesign**, March, 2005, pp. 18-26.

[30] WASHIO, K. *et al*. 82 GHz Dynamic Frequency Divider in 5.5ps ECL SiGe HBTs. In: 2000 IEEE International Solid-State Circuits Conference, 2000, San Francisco. **Digest…** California, USA: IEEE Solid-State Circuits Society, 2000, pp.210-211.

[31] SOKOLICH, M. *et al*. A Low-Power 72.8-GHz Static Frequency Divider in AlInAs/InGaAs HBT Technology. **IEEE Journal of Solid-State Circuits**, vol. 36, no. 9, September, 2001, pp. 1328-1333.

[32] WASHIO, K. *et al*. 67-GHz Static Frequency Divider Using 0.2-μm Self-Aligned SiGe HBTs. **IEEE Transactions on Microwave Theory and Techniques**, vol. 49, no. 1, January, 2001, pp. 3-7.

[33] UMEDA Y. *et al*. Over-60-GHz Operation of SCFL Dynamic Frequency Divider Using InP-Based HEMTs. In: 1997 IEEE MTT-S International Microwave Symposium, 1997, Denver. **Digest…** Colorado, USA: IEEE Microwave Theory and Techniques Society, 1997, pp. 457-460.

[34] JENSEN, J. F. *et al*. 39.5-GHz Static Frequency Divider Implemented in AlInAs/GaInAs HBT Technology. In: 1992 IEEE GaAs IC Symposium, 1992, Miami Beach. **Digest…** Florida, USA: IEEE Electron Devices Society, 1992, pp. 101-104.

[35] LAO, Z. *et al*. 35-GHz Static and 48-GHz Dynamic Frequency Divider IC's Using 0.2-μm ALGaAs/GaAs-HEMTs, **IEEE Journal of Solid-State Circuits**, vol. 32, no. 10, October, 1997, pp. 1556-1562.

[36] FELDER, A. *et a*l. 46 Gb/s DEMUX, 50 Gb/s MUX, and 30 GHz Static Frequency Divider in Silicon Bipolar Technology. **IEEE Journal of Solid-State Circuits**, vol. 31, no. 4, April, 1996, pp. 481-486.

[37] KURISU, M. *et al*. A Si Bipolar 28 GHz Dynamic Frequency Divider. In: 1992 IEEE International Solid-State Circuits Conference, 1992, San Francisco. **Digest…** California, USA: IEEE Solid-State Circuits Society, 1992, pp. 92-93.

[38] WETZEL, M. *et al*. A 26.5 GHz Silicon MOSFET 2: Dynamic Frequency Divider. **IEEE Microwave and Guided Wave Letters**, vol. 10, no. 10, October, 2000, pp. 421-423.

[39] ICHINO, H. *et al*. 18-GHz 1/8 Dynamic Frequency Divider Using Si Bipolar Technologies. **IEEE Journal of Solid-State Circuits**, vol. 24, no. 6, December, 1989, pp. 1723-1728.

[40] YU, X. P. *et al*. 1V 10 GHz CMOS Frequency Divider with Low Power Consumption. **Electronics Letters**, vol. 40, no. 8, 15th April, 2004, pp. 467-469.

[41] KURISU, M. *et al*. A Si Bipolar 28-GHz Dynamic Frequency Divider. **IEEE Journal of Solid-State Circuits**, vol. 27, no. 12, December, 1992, pp. 1799-1804.

[42] SOARES, R. (Ed.). **GaAs MESFET Circuit Design**. Norwood, MA, USA: Artech House, 1988, pp. 525-528.

[43] KUDSZUS, S. 94/47-GHz Regenerative Frequency Divider MMIC with Low Conversion Loss. **IEEE Journal of Solid-State Circuits**, vol. 35, no. 9, September, 2000, pp. 1312-1317.

[44] DESGREG, S. *et al*. Wide-Bandwidth Ku-Band Monolithic Analog Frequency Divider. **IEEE Microwave and Guided Wave Letters**, vol. 8, no. 2, February, 1998, pp. 84-86.

[45] AMINE, H. *et al*. New Approach to GaAs MESFET Analog Frequency Dividers with Low Threshold Input Power and High Conversion Gain. **IEEE Transactions on Microwave Theory and Techniques**, vol. 40, no. 12, December, 1992, pp. 2345-2351.

[46] KAMINSKY, D. *et al*. A Dual-Gate GaAs F.E.T. Analog Frequency Divider. In: 1983 IEEE MTT-S International Microwave Symposium, 1983, Boston. **Digest…** Massachusetts, USA: IEEE Microwave Theory and Techniques Society, 1983, pp. 352-354.

[47] SARKISSIAN, J. C. *et al*. A 60-GHz HEMT-MMIC Analog Frequency Divider by Two. **IEEE Journal of Solid-State Circuits**, vol. 30, no. 10, October, 1995, pp. 1062-1067.

[48] SUÁREZ, A. *et al*. Large Signal Design of Broadband Monolithic Microwave Frequency Dividers. In: 1992 IEEE MTT-S International Microwave Symposium, 1992, Albuquerque. **Digest...** New Mexico, USA: IEEE Microwave Theory and Techniques Society, 1992, pp. 1595-1598.

[49] HONJO, K.; MADIHIAN, M. Novel Design Approach for X-Band GaAs Monolithic Analog ¼ Frequency Divider. **IEEE Transactions on Microwave Theory and Techniques**, vol. MTT-34, no. 4, April, 1986, pp. 436-441.

[50] KANAZAWA, K. *et al*. A 15 GHz Single Stage GaAs Dual-Gate FET Monolithic Analog Frequency Divider with Reduced Input Threshold Power. **IEEE Transactions on Microwave Theory and Techniques**, vol. 36, no. 12, December, 1988, pp. 1908-1912.

[51] ANGELOV, I. *et al*. 48/24 GHz and 20/10 GHz Regenerative Frequency Dividers. In: 1996 IEEE MTT-S International Microwave Symposium, 1996, San Francisco. **Digest…** California, USA: IEEE Microwave Theory and Techniques Society, 1996, pp.971-974.

[52] BRANGER, B. *et al*. Comparison Between Simulations and Measurements of Large Signal and Nonlinear Noise Behaviors of MMIC Analog Frequency Divider by Two. **IEEE Microwave and Guided Wave Letters**, vol. 7, no. 5, May, 1997, pp. 127-129.

[53] LANDÉN, L.; FAGER, C.; ZIRATH, H. Regenerative GaAs MMIC Frequency Dividers for 28 and 14 GHz. In: 30th European Microwave Conference, EuMC 2000, 2000, Paris. **Digest…** France: IEEE MTT-S/GAAS/EuMA, 2000, pp. 1-3.

[54] HARRISON, R. G. Theory of Regenerative Frequency Dividers. In: 1989 IEEE International Microwave Symposium, 1989, Long Beach. **Digest…** California, USA: IEEE Microwave Theory and Techniques Society, 1989, pp. 459-462.

[55] DERKSEN, R. H.; LÜCK, V.; REIN, H. M. Stability Ranges of Regenerative Frequency Dividers Employing Double Balanced Mixers in Large-Signal Operation. **IEEE Transactions on Microwave Theory and Techniques**, vol. 39, no. 10, October, 1991, pp. 1759-1762.

[56] DERKSEN, R. H.; REIN, H. M; WÖRNER, K. Monolithic Integration of 5.3 GHz Regenerative Frequency Divider using a Standard Bipolar Technology. **Electronics Letters**, vol. 21, no. 22, 24th October, 1985, pp. 1037-1039.

[57] DERKSEN, R. H.; REIN, H. M. 7.3-GHz Dynamic Frequency Dividers Monolithically Integrated in a Standard Bipolar Technology. **IEEE Transactions on Microwave Theory and Techniques**, vol. 36, no. 3. March, 1988, pp. 537-541.

[58] SHAN, C. R.; TAI, Z. Q.; YUNG, E. K. N. Dual-Gate FET Millimeter-Wave Frequency Dividers. **Microwave and Optical Technology Letters**, vol. 14, no. 4, March, 1997, pp. 210-213.

[59] DRISCOLL, M. M. Phase Noise Performance of Analog Frequency Dividers. **IEEE Transactions on Ultrasonics, Ferroelectrics and Frequency Control**, vol. 37, no. 14, July, 1990, pp. 295-301.

[60] LLOPIS, O. *et al*. Analytical Model of Noise in an Analog Frequency Divider. In: 1993 IEEE MTT-S International Microwave Symposium, 1993, Atlanta. **Digest…** Georgia, USA: IEEE Microwave Theory and Techniques Society, 1993, pp. 1033-1036.

[61] PIKAL, E. S. F.; WALLS, F. L. Low PM Noise Regenerative Dividers. In: 1997 IEEE International Frequency Control Symposium, Orlando. **Proceedings…** Florida, USA: IEEE Ultrasonics, Ferroeletrics, and Frequency Control Society, 1997, pp. 478-484.

[62] PIKAL, E. S. F.; WALLS, F. L. Microwave Regenerative Frequency Dividers with Low Phase Noise. **IEEE Transactions on Ultrasonics, Ferroelectrics, and frequency Control**, vol. 46, no. 1, January 1999, pp. 216-219.

[63] GUPTA, A. S.; NAVA, F. G.; WALLS, F. L. Conjugate Regenerative Dividers. **IEEE Transactions on Ultrasonics, Ferroelectrics, and Frequency Control**, vol. 51, no. 3, March, 2004, pp. 271-276.

[64] PENFIELD, P.; RAFUSE, R. P. **Varactor Applications**. Cambridge, MA, USA: MIT Press, 1962. pp. 436-483.

[65] HARRISON, R. G. A. Broadband Frequency Divider in Waveguide. In: 1978 IEEE MTT-S International Microwave Symposium, 1978, Ottawa. **Digest…** Canada: IEEE Microwave Theory and Techniques Society, 1978, pp. 257-259.

[66] HARRISON, R. G. Theory of the Varactor Frequency Halver. In: 1983 IEEE MTT-S International Microwave Symposium, 1983, Boston. **Digest…** Massachusetts, USA: IEEE Microwave Theory and Techniques Society, 1983, pp. 203-205.

[67] KALIVAS, G. A.; HARRISON, R. G. A New Slotline-Microstrip Frequency Halver. In: 1985 IEEE MTT-S International Microwave Symposium, 1985, St. Louis. **Digest…** Missouri, USA: IEEE Microwave Theory and Techniques Society, 1985, pp. 683-686.

[68] HARRISON, R. G.; CORNISH, W. D. Varactor Frequency Halver with Enhanced Bandwidth and Dynamic Range. In: 1986 IEEE MTT-S International Microwave Symposium, 1986, Baltimore. **Digest…** Maryland, USA: IEEE Microwave Theory and Techniques Society, 1986, pp. 305-308.

[69] NATIV, Z. The Application of a Frequency Multiplier Design Method to the Design of Microwave Parametric Dividers. **IEEE Transactions on Microwave Theory and Techniques**, vol. MTT-35, no. 2, February, 1987, pp. 189-194.

[70] NATIV, Z. A New Reversible Varactor Frequency Halver/Doubler Device. In: 1987 IEEE MTT-S International Microwave Symposium, 1987, Las Vegas. **Digest…** Nevada, USA: IEEE Microwave Theory and Techniques Society, 1987, pp.921-924.

[71] MANLEY, J. M.; ROWE, H. E. Some General Properties of Nonlinear Elements, Part I, General Energy Relations. **Proceedings of IRE**, vol. 44, July, 1956, pp. 904-913.

[72] HAYASHI, C.; NISHIKAWA, Y.; ABE, M. Subharmonic Oscillations of Order One Half. **IRE Transactions on Circuit Theory**, vol. CT-7, June, 1960, pp. 102-111.

[73] LIPPARINI, A.; MARAZZI, E.; RIZZOLI, V. Computer-Aided Design of Microwave Parametric Frequency Dividers. In: 1981 IEEE MTT-S International Microwave Symposium, 1981, Los Angeles. **Digest…** California, USA: IEEE Microwave Theory and Techniques Society, pp. 229-231.

[74] LIPPARINI, A.; MARAZZI, E.; RIZZOLI, V. A New Approach to the Computer-Aided Design of Nonlinear Networks and its Applications to Microwave Parametric Frequency Dividers. **IEEE Transactions on Microwave Theory and Techniques**, vol. MTT-30, July, 1982, pp. 1050-1058.

[75] STEZER, F. Microwave Parametric Subharmonic Oscillations for Digital Computing. **Proceedings of IRE**, vol. 47, August, 1959, pp. 1317-1324.

[76] ONYSHKEVYCH, L. S.; KOSONOCKY, W. F.; LO, A. W. Parametric Phased-Locked Oscillator – Characteristics and Applications to Digital Systems. **IRE Transactions on Electronic Computers**, vol. EC-8, September, 1959, pp. 277-286.

[77] RIZZOLI, V.; LIPPARINI, A. General Stability Analysis of Periodic Steady-State Regimes in Nonlinear Microwave Circuits. **IEEE Transactions on Microwave and Techniques**, vol. MTT-33, January, 1985, pp. 30-37.

[78] SLOAN, G. R. The Modeling, Analysis and Design of Filter-Based Parametric Frequency Dividers. IEEE **Transactions on Microwave and Techniques,** vol. MTT-41, February, 1983, pp. 224-228.

[79] HARRISON, R. G. **Broadband Frequency Divider Using Microwave Varactor**. USA Patent no. 4,152,680, 1st May 1979.

[80] HARRISON, R. G. **Broadband Frequency Divider in Waveguide**. USA Patent no. 4,228,411, 14 October 1980.

[81] CORNISH, W. D.; GAUTIER, F. A. **Broadband Frequency Divider**. USA Patent no. 4,334,202, 8 June 1982.

[82] CORNISH, W. D.; HEWIT, L. B. **Self-Biasing Broadband Frequency Divider**. USA Patent no. 4,533,886, 6 August 1985.

[83] HARRISON, R. G.; KALIVAS, G. **A. Microstrip/Slotline Frequency Halver**. USA Patent no. 4,636,757, 13 January 1987.

[84] SATO, R.; CRISTAL, E. G. Simplified Analysis of Coupled Transmission-Line Networks. IEEE **Transactions on Microwave and Techniques**, vol. MTT-18, no. 3, March, 1970, pp. 122-131.

[85] WEISS, J. A.; BRYANT, T. G. Even and Odd Mode Characteristic Impedance for Coupled Microstrip. In: **Microwave Engineer's Handbook**, vol. 1. Dedham, MA, USA: Artech House, 1971, pp. 132-133.

[86] HOUDART, M.; AURY, C. Various Excitation of Coplanar Waveguide. In: 1983 IEEE MTT-S International Microwave Symposium, 1983, Boston. **Digest…** Massachusetts, USA: IEEE Microwave Theory and Techniques Society, 1983, pp.116-118.

[87] CORNISH, W. D. **Broadband Microwave Frequency Divider for Division by Numbers Greater than Two**. USA Patent no. 4,266,208, 5 May 1981.

[88] CORNISH, W. D. **Non-resonant Microwave Frequency Halver**. USA Patent no. 4,609,893, 2nd September 1986.

[89] JACOB, A.; JENETT, M. Performance of Microwave FM Signal Frequency Division Circuits. In: 1985 IEEE MTT-S International Microwave Symposium Digest, 1985, St. Louis. **Digest…** Missouri, USA: IEEE Microwave Theory and Techniques Society, 1985, pp. 207-210.

[90] CORNISH, W. D. **Wideband MESFET Microwave Frequency Divider**. USA Patent no. 4,327,343, 27 April 1982.

[91] STAPLETON, S.; STUBBS, M. GaAs Monolithic Frequency Halver. **Electronics Letters**, vol. 22, no. 15, 1986, pp. 173-174.

[92] HESHMATI, Z.; HUNTER, I.; POLLARD, R. PHEMT Frequency Dividers. In: 2nd Electromagnetic Remote Sensing Defense Technology Centre Technical Conference, 2005, Edinburgh. **Proceedings…** Edinburgh, England: EMRS DTC, 2005, A16.

[93] HESHMATI, Z.; HUNTER, I.; POLLARD, R. PHEMT Frequency Dividers. In: 3rd Electromagnetic Remote Sensing Defense Technology Centre Technical Conference, 2006, Edinburgh. **Proceedings…** Edinburgh, England: EMRS DTC, 2006, A17.

[94] HESHMATI, Z.; HUNTER, I.; POLLARD, R. Microwave Parametric Frequency Dividers with Conversion Gain. **IEEE Transactions on Microwave and Techniques**, vol. MTT-55, no. 10, October, 2007, pp. 2059-2064.

[95] TSUI, J. B. Y.; STEPHENS, J. P. Digital Microwave Receiver Technology. **IEEE Transactions on Microwave and Techniques**, vol. MTT-50, no. 3, March, 2002, pp. 699-705.

[96] MALLAVARPU, R.; TEETER, D.; SNOW, M. The Importance of Gate Charge Formulation in Large-Signal PHEMT Modeling. In: 1998 IEEE MTT-S International Microwave Symposium, 1998, Baltimore. **Digest…** Maryland, USA: IEEE Microwave Theory and Techniques Society, 1998, pp. 87-90

[97] QUÉRÉ, R.; NGOYA, E.; CAMIADE, M. Large Signal Design of Broadband Monolithic Microwave Frequency Dividers and Paced-Locked Oscillators. **IEEE Transactions on Microwave and Techniques**, vol. MTT-41, no. 11, November, 1993, pp. 1928-1938.

[98] AGILENT TECHNOLOGIES. **Advanced Design System:** Capability Overview. Disponível em <http://www.agilent.com/find/eesof>. Acesso em: 15 jun. 2006.

[99] AGILENT TECHNOLOGIES. **ADS Circuit Models**. Disponível em <http://eesof.tm.agilent.com/cgi-bin>. Acesso em 15 out. 2006.

[100] McCAMANT, A. J. *et al.* An Improved GaAs MESFET Model for SPICE. **IEEE Transactions on Microwave and Techniques**, vol. MTT-38, no. 6, June, 1990, pp. 822-824.

[101] DAWANDE, M. Dynamic Characterization and Model of High Frequency FET Devices. In: 2004 Topical Meeting on Silicon Monolithic Integrated Circuits in RF Systems, 2004, Atlanta. **Digest…** Georgia, USA: IEEE Microwave Theory and Techniques Society, 2004, pp. 195-198.

[102] FILTRONIC COMPOUND SEMICONDUCTOR LTD. **FPD1500SOT89**: Low Noise High Linearity Packaged pHEMT – Datasheet v3.0. Disponível em <http://www.filtronic.com>. Acesso em 10 out. 2007.

[103] FILTRONIC COMPOUND SEMICONDUCTOR LTD. **Modeling Report Discrete Package Models**, version 1.0, 10/02/2006. Fornecido por correio eletrônico pelo fabricante.

[104] FILTRONIC COMPOUND SEMICONDUCTOR LTD. **Modeling Report FPD1500 TOM3 and TOM2 Models**, version 1.0, 27/01/2005. Disponível em <http://www.filtronic.com>. Acesso em 10 out. 2004.

[105] GUPTA, K. C.; GARG, R.; BAHL, I. J. **Microstrip Lines and Slotlines**. 2nd ed. Boston/London: Artech House, 1996, pp. 324-328.

[106] ANG, K. S.; LEONG, Y. C.; LEE, C. H. Multisection Impedance-Transforming Coupled-Line Baluns. **IEEE Transactions on Microwave and Techniques**, vol. MTT-51, no. 2, February, 2003, pp. 536-541.

[107] COLLIN, R. E. **Foundations for Microwave Engineering**: International Student Edition. Tokyo, Japãn: McGraw-Hill Kogakusha, Ltd., 1996, pp. 313-320.

[108] FILTRONIC COMPOUND SEMICONDUCTOR LTD. **Modeling Report Implementing the TOM Models in ADS**, version 1.0", 24/01/2005. Disponível em <http://www.filtronic.com>. Acesso em 10 out. 2007.

[109] CRISTAL, E. G.; MATHAEI, G. L. A Technique for Design of Multiplexers Having Contiguous Channels. IEEE **Transactions on Microwave and Techniques**, vol. MTT-12, no. 1, January, 1964, pp. 88-93.

[110] GUPTA, K. C.; GARG, R.; BAHL, I. J. **Microstrip Lines and Slotlines**. 2nd ed. Boston/London: Artech House, 1996, p. 489.

[111] POZAR, D. M. **Microwave Engineering**. 2nd ed. [ ]: John Wiley & Sons, 2003.

[112] AGILENT TECHNOLOGIES. **LineCalc**: ADS2006U3 Documentation. May, 2007.

[113] AGILENT TECHNOLOGIES. **Smith Chart Utility**: ADS2006U3 Documentation. May, 2007,.

[114] WADELL, B. C. **Transmission Line Design Handbook**. Boston/London: Artech House, 1991, pp. 311-312.

[115] AGILENT TECHNOLOGIES. **Transient/Convolution Simulation**: ADS2005A Documentation. August, 2005.

[116] MAYARAM, K. *et al.* Computer-Aided Circuit Analysis Tools for RFIC Simulation: Algorithms, Features, and Limitations. **IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing**, vol. 47, no. 4, April, 2000, pp. 274-286.

[117] AGILENT TECHNOLOGIES. **Guide to Harmonic Balance Simulation in ADS**: ADS2005A Documentation. August, 2005,.

[118] SUÁREZ, A.; QUÉRÉ, R. **Stability Analysis of Nonlinear Microwave Circuits**. Boston, MA, USA: Artech-House, 2003.

[119] RAMÍREZ, F.; SUÁREZ, A.; SANCHO, S. Stabilization Techniques for Frequency Dividers. **IEEE Microwave and Wireless Components Letters**, vol. 14, no. 5, May 2004, pp. 192-194.

[120] GONZÁLEZ, O. *et al*. Analysis of Frequency Division in Microstrip Circuits by Using the FDTD Method. **Microwave and Optical Technology Letters**, vol. 50, no. 5, May, 2008, pp. 1300-1302.

[121] ANAKABE, A. *et al*. Analysis of Microwave Frequency Dividers in Harmonic-Balance Simulators. In: The 8th IEEE International Conference

on Electronics, Circuits and Systems, ICECS 2001, 2001, Malta. **Proceedings…** Malta: IEEE CAS Society/Region 8, 2001, vol. 3, pp. 1575-1578.

[122] AGILENT TECHNOLOGIES. **In-fixture microstrip device measurements using TRL* calibration**. Product Note Agilent 8720-2. USA, 2000.

[123] AGILENT TECHNOLOGIES. **In-Fixture Measurements Using Vector Network Analyzer**. Application Note Agilent AN1287-9. USA, 2006.

[124] MAGRI, V. P. R. **Integridade de Sinais em Placas de Circuito Impresso de Altas Taxas**. Rio de Janeiro, 2007. pp. 62-64. Dissertação (Mestrado em Engenharia Elétrica) - Departamento de Engenharia Elétrica, Centro Técnico Científico, Pontifícia Universidade Católica do Rio de Janeiro (PUC-Rio).